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[7-Series FPGA  
Overview](#)

**7-Series FPGA  
Overview by  
XilinxInc 8 years ago  
28 minutes 7,551  
views A high-level ,  
introduction , to the  
, 7 Series , product  
family and all of its  
device features. For  
additional video and**

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**instructor-led**

## [7-Series Clocking Resources](#)

**7-Series Clocking Resources by XilinxInc 8 years ago 50 minutes 8,866 views** Learn the details of the dedicated , 7 Series , clocking resource. After completing

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**this module, you will  
be able to describe  
the**

**UltraScale+ vs. 7  
Series FPGAs:  
Doubling  
Performance/Watt**

**UltraScale+ vs. 7  
Series FPGAs:  
Doubling  
Performance/Watt  
by XilinxInc 4 years**

*Page 6/44*

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ago 4 minutes, 59

seconds 1,702 views

This video

highlights the

performance, power

and flexibility

offered by the dual-

operating voltage in

, Xilinx , 16nm

Kintex

[7 Series DSP](#)

[Resources](#)

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**7 Series DSP**

**Resources by**

**XilinxInc 8 years ago**

**27 minutes 4,413**

**views This video**

**introduces the DSP**

**slice features of the**

**, 7 Series FPGAs , .**

**In addition**

**discusses the Pre-**

**Adder and Dynamic**

**Pipeline**

**[HES 7 7 Series](#)**

*Page 8/44*



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## [FPGA Programming](#)

**HES 7 7 Series**

**FPGA Programming**

**by aldecinc 8 years**

**ago 7 minutes, 20**

**seconds 772 views**

**The , 7 , -, series**

**FPGA , devices on**

**Aldec's ASIC**

**prototyping platform**

**can be programmed**

**using two different**

**methods: using a**

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["7 series FPGA"](#)

**"7 series FPGA" by  
PALLETS Channel 3  
years ago 2 minutes,  
52 seconds 136  
views We would like  
to introduce ,  
7series FPGA , . We  
also introduce new  
product, Spartan 7.  
--- PALLETS**

**Website <http://pallet>**

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[s-go.com/](https://www.s-go.com/)

**[MAX 10 FPGA](#)**  
**[Overview](#)**

**MAX 10 FPGA  
Overview by Intel  
FPGA 6 years ago 8  
minutes, 42 seconds  
17,532 views Max 10  
, FPGA , family  
combines the ease  
of use of , FPGAs ,  
with new features**

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**including an ADC,  
dual image  
configuration with**

**[FPGA audio – OPL-  
like FM operator  
synthesis !](#)**

**FPGA audio – OPL-  
like FM operator  
synthesis ! by Bits  
inside by René Rebe  
Streamed 1 year ago  
57 minutes 494**

*Page 12/44*

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**views It's time for  
some more random  
#, FPGA , #FM #OPL-  
like synthesis and  
Verilog code work.  
#Ad: FM synth ,  
books \u0026 , more  
@Amazon:**

**[1-bit FPGA DAC  
working! Playing  
Music via the serial  
port! RISC-V FPGA](#)**

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**1-bit FPGA DAC**

**working! Playing**

**Music via the serial**

**port! RISC-V FPGA**

**by Bits inside by**

**René Rebe**

**Streamed 1 year ago**

**1 hour, 18 minutes**

**1,136 views**

**Streaming 8-bit**

**#PCM audio over the**

**UART, and later**

**Flash to our 1-bit**

**#DAC PicoSOC #,**

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**FPGA , #RISCV**

**design! #Ad: audio**

**[FPGA Bitcoin](#)**

**[Mining: Intro](#)**

**FPGA Bitcoin**

**Mining: Intro by**

**FPGA Zealot**

**Streamed 1 month**

**ago 37 minutes 954**

**views Looking at the**

**basics of bitcoin**

**mining on , FPGAs ,**

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. Twitch: [https://www.twitch.tv/fpga\\_zealot](https://www.twitch.tv/fpga_zealot)  
Website:

[Creating Custom PYNQ Overlay with VIVADO HLS, IPI and Jupyter](#)

**Creating Custom PYNQ Overlay with VIVADO HLS, IPI and Jupyter by Digitronix Nepal 2**

*Page 16/44*



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years ago 22

minutes 3,704 views

This session is on

Creating the custom

Overlay using the

VIVADO HLS,

VIVADO IP

integrator and the

Jupyter interface.

[Chapter 7](#)

[Introduction](#)

**Chapter 7**

*Page 17/44*

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**Introduction by**

**Scott Stevens 7**

**years ago 4 minutes,**

**16 seconds 2,076**

**views Chapter , 7**

**Introduction , from**

**\", Introduction , to**

**Statistics, Think ,**

**\u0026 , Do\" by**

**Scott Stevens ([http://](http://www.StevensStats.com)**

**[/www.StevensStats.](http://www.StevensStats.com)**

**com) Textbook from**

**[FPGA Audio](#)**

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[Synthesizer final project](#)

**FPGA Audio  
Synthesizer final  
project by Alec  
Steinkraus 4 years  
ago 6 minutes, 55  
seconds 22,018  
views This is our  
final project for ECE  
385 Digital Systems  
Laboratory at  
University of Illinois**

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**Urbana-Champaign  
(SP 2017) Code can**

**[U.S. vs China: The  
Battle for Bitcoin  
Mining Supremacy |  
WSJ](#)**

**U.S. vs China: The  
Battle for Bitcoin  
Mining Supremacy |  
WSJ by Wall Street  
Journal 2 months  
ago 8 minutes, 24**

*Page 20/44*

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**seconds 1,378,497**

**views Chinese**

**bitcoin miners have**

**long dominated the**

**global processing**

**power that runs the**

**bitcoin network with**

**sophisticated**

**[\[004\] CHIP \\$9](#)**

**[computer, and](#)**

**[PocketCHIP](#)**

**[004] CHIP \$9**

*Page 21/44*

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computer, and

**PocketCHIP** by

**OpenTechLab** 4

years ago 59

minutes 269,014

views Review and

experiments with

the **CHIP** \$9 mini-

computer, and the

**PocketCHIP** plug-in

for the **CHIP** that

makes it into a

**mobile**

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[VCF East 2019 -- Bil Herd -- Component Aging and Heat](#)

**VCF East 2019 -- Bil Herd -- Component Aging and Heat by Vintage Computer Federation 1 year ago 1 hour, 11 minutes 6,662 views Bil Herd: Component Aging and Heat Filmed at**

*Page 23/44*

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**the Vintage**

**Computer Festival**

**East, May 3, 2019 at**

**InfoAge Technology**

**Center,**

**[47\) Getting started  
with FPGA's using  
iCEBreaker](#)**

**47) Getting started  
with FPGA's using  
iCEBreaker by The  
Current Source 2**

*Page 24/44*



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years ago 11

minutes, 54 seconds

34,157 views I've

been looking around

at , FPGA , dev

boards in order to

branch out from the

usual micro

controller world and

get into some more

[Albert Einstein said:](#)

[\"Everything is](#)

[vibration\"](#)

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**Albert Einstein said:  
\"Everything is  
vibration\" by  
Dyslexic Artist  
Theory on the  
Physics of 'Time' 8  
years ago 8 minutes,  
30 seconds 70,937  
views Albert  
Einstein said:  
\"Everything is  
vibration\" But if this  
is true if everything**

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**is just a form of  
energy vibration  
then there should be  
a**

**[how to write a  
resume with little or  
no job experience](#)**

**how to write a  
resume with little or  
no job experience  
by Emme Sea 9  
years ago 9 minutes,**

*Page 27/44*

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**25 seconds**

**1,015,815 views** How to write a resume when you have little or no work experience but a school degree. If you find this video useful, please click

**[Creative Ideas to Contact Aliens - SETI's Dan Werthimer](#)**

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**Creative Ideas to  
Contact Aliens -  
SETI's Dan  
Werthimer by  
FORA.tv 11 years  
ago 2 minutes, 4  
seconds 16,591  
views Complete  
video at: [http://fora.t  
v/2009/05/30/Dan\\_W  
erthimer\\_Presents\\_  
SETIhome Dan  
Werthimer, chief](http://fora.tv/2009/05/30/Dan_Werthimer_Presents_SETIhome_Dan_Werthimer_chief)**

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**scientist at  
SETI@home,**

[Introduce Yourself -  
Talk About Yourself  
& Ask About  
What Others Like |  
English Speaking  
Practice](#)

**Introduce Yourself -  
Talk About Yourself  
& Ask About  
What Others Like |**

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**English Speaking**

**Practice by Mark**

**Kulek 7 years ago 2**

**minutes, 4 seconds**

**1,993,822 views**

**Learn English - Mark**

**Kulek ESL. Learn to**

**introduce yourself,**

**talk about your likes**

**and what you do.**

**You will also learn**

**to ask**

**[Mickey \u0026](#)**

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[Donald 3 - Game Over \(SNES\)](#)

**Mickey \u0026**

**Donald 3 - Game**

**Over (SNES) by**

**romnintendo 2**

**weeks ago 52**

**seconds 881 views**

**Mickey , \u0026 ,**

**Donald 3 (Disney's**

**Magical Quest 3**

**Starring Mickey ,**

**\u0026 , Donald) was**



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released for the  
Super Famicom in  
1995, in Japan

[Quantum  
Computation for  
Chemistry and  
Materials](#)

**Quantum  
Computation for  
Chemistry and  
Materials by HRL  
Laboratories, LLC 1**

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year ago 57 minutes

1,837 views Dr.

Jarrold McClean

Google's Quantum

Artificial Intelligence

Lab Quantum

computers promise

to dramatically

advance our

[\[013-1\] Open Source](#)

[FPGA Synthesis](#)

[with the icoBoard -](#)

[part 1](#)

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**[013-1] Open Source  
FPGA Synthesis  
with the icoBoard -**

**part 1 by**

**OpenTechLab 3**

**years ago 20**

**minutes 12,459**

**views Twitter:**

**@OpenTechLabCha**

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**echlab@mstdn.io**

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[r.com/opentechlab](http://r.com/opentechlab)

[A Full Break of  
Bitstream  
Encryption of Xilinx  
7-Series FPGAs |  
Maik Ender |  
Hardwear.io Virtual  
Con](#)

**A Full Break of  
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Overview Ucy

**Maik Ender |**

**Hardwear.io Virtual  
Con by hardwear.io**

**1 year ago 38**

**minutes 1,045 views**

**Talk Abstract:**

----- The recent discovery of the Starbleed bug is a serious threat to a widely used family of , FPGA ,

**[Introduction to the](#)**

*Page 37/44*

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[Xilinx Zynq-7000 All Programmable SoC Architecture](#)

**Introduction to the Xilinx Zynq-7000 All Programmable SoC Architecture by Major Adam 4 years ago 23 minutes 6,274 views This video provides an , introduction , to the , Xilinx , Zynq-7000**

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**All Programmable  
SoC Architecture.  
This video will  
review the general**

**[Vector Control of  
Drives Day 1](#)**

**Vector Control of  
Drives Day 1 by  
CUSP 4 months ago  
5 hours, 43 minutes  
181 views**

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[UltraScale ASIC-like  
clocking](#)

**UltraScale ASIC-like  
clocking by XilinxInc**

**7 years ago 10**

**minutes, 19 seconds**

**3,021 views Learn**

**about the new**

**UltraScale ASIC-like  
clocking**

**architecture: how it**

**can be used, the**

**benefits it brings**



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and how easy it is  
to

[FPGA MIDI Music  
Synthesizer](#)

**FPGA MIDI Music  
Synthesizer by  
element14 presents  
2 years ago 16  
minutes 30,161  
views Have you ever  
wondered how  
digital synthesizers**

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**work? In today's  
video, Andy shows  
how to build one  
with just a handful  
of parts**

**[Memory Systems -  
Lecture 2.1: Memory  
Scaling, Flash  
Memory, and DRAM  
Refresh \(Technion,  
Summer 2018\)](#)**

**Memory Systems -**

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**Lecture 2.1: Memory  
Scaling, Flash**

**Memory, and DRAM**

**Refresh (Technion,**

**Summer 2018) by**

**Onur Mutlu Lectures**

**2 years ago 49**

**minutes 481 views**

**Memory Systems -**

**Lecture 2.1: Memory**

**Scaling, Flash**

**Memory, and DRAM**

**Refresh (Technion,**

**Summer 2018) (a)**

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**Memory**

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